A1	Personal details

Name: M.Duraisamy

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        A. Thalaiyur (PO), Sankari (TK),
        Salem-637103
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OBJECTIVE:
To seek a challenging position so as to utilize my skill and abilities in a field, with dedication and determination towards achievement of the institution's goal, besides sharpening my skills for further career advancement and contribution.

A2	Areas of Interest	VLSI Design

A3	Subjects Taught	
UG	**PG**
Circuit Analysis	
Electronic Devices and Circuits	
Transmission lines and Waveguides	
Microprocessors and Microcontroller	
Embedded Systems	
Digital Principles and System Design	
Digital Electronics	
Electronic Circuits-I	
Electronic Circuits-II	
Wireless Communications	
Antennas and wave propagation	

A4	Academic Background			
Degree	**Specialization**	**Name of the Institute**	**University**	**Month and Year of Passing**
M.E.	VLSI Design	Sengunthar Engineering College	Anna University	April 2006
B.E.	ECE	K.S.R College of Engineering	Anna University	April 2012

A5	Work Experience as on July 2023			
Name of the Institution	**Position**	**From**	**To**	
Knowledge Institute of Technology	Assistant Professor	03.07.2023	Till date	
Dhirajlal Gandhi College of Technology	Assistant Professor	11.03.2022	30.06.2023	
Muthayammal Engineering College (Autonomous)	Assistant Professor	01.07.2016	30.06.2021	
Sri Shanmugha college of Engineering and Technology	Assistant Professor	09.07.2014	30.04.2015	
Sengunthar Engineering College	Assistant Professor	19.07.2012	26.05.2014	
Sengunthar Engineering College	Lecturer	10.08.2009	30.06.2010	

Total Experience	10 Yrs	**Teaching**	10 Yrs	**Research**		**Industry**	

A6	Research Publications				
Total Number of Publications	**International Journal**	**National Journal**	**International Conference**	**National Conference**	**Total**
	3		2	1	6
Recent Publications (Journals)					
1	"Wildlife and Train Collision Detection System by WSN", International Journal of Advances in Engineering and Management (IJAEM), ISSN: 2395-5252, Volume 2, Issue 3, pp: 295-299.				
2	"Hazard Monitoring System in Industry Using Embedded Systems", International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET), e-ISSN: 2319-8753, p-ISSN: 2320-6710, Volume 9, Issue 7, July 2020.				
3	"Talking Smart cap for Visually Impaired Person", International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET), e-ISSN: 2319-8753, p-ISSN: 2320-6710, Volume 9, Issue 8, August 2020.				
Recent Publications (Conferences)					
1	Participated in International Conference on Advances in IOT, High Performance and Edge Computing Organized by ICT Academy, held between 14-18 Oct 2020.				
2	Presented a Technical paper on "Smart Garbage Monitoring system using IOT " in the International Conference "ISETSD-2018" held on 30th & 31st March 2018 at Muthayammal Engineering College (Autonomous), Rasipuram, Tamilnadu, India.				

A7	Memberships in Professional Bodies
S.No.	**Name of the Professional Body**
1	Institution of Electronics and Telecommunication Engineers (IETE) - Lifetime Member – **AM 500677**

A8	Programs Organised
	Value Added Course- 1 STTP- 1 Technical Symposium- 2 National Level Workshop-2

A9	Highlights of Major Contributions
1	Motivated students to achieve their goals
2	Department Internal Exam Cell Coordinator
3	Department AICTE and Anna University Coordinator
4	NBA Criterion V In-charge
5	Department Library in-charge

A 10	Training Programmes Attended
1	Participated in AICTE Sponsored Six Days Virtual Short Term Training Programme (STTP) on "Applications **of Artificial Intelligence Towards 4.0**" from Feb 15-20, 2021.
2	Participated in a One-week online Faculty Development Programme on **"Academic Leadership Enhancement in Technical Institutions"** organized by **Electronics and Communication Engineering Department NITTTR, Chandigarh** from May 04-08, 2020.
3	Participated in a One-week online Faculty Development Programme on **"Image and Embedded Processing"** organized by **Electronics and Communication Engineering Department NITTTR, Chandigarh** from May 11-15, 2020.
4	Participated in a One-week online Faculty Development Programme on **"Research Trends in VLSI Design"** organized by **Electronics and Communication Engineering Department NITTTR, Chandigarh** from May 25-29, 2020.
5	Participated in a One-week Faculty Development Programme on **"VLSI Chip Design Hands-on using open source EDA"** jointly organized by **Electronics and ICT Academies** from December 16-20 , 2019.
6	Participated in National Level Short Term Training Programme (STTP) on "**Green Communication Networks and Simulation of Wireless Networks using NS-3**" sponsored by AICTE held on 01.12.2019 to 07.12.2019 at **S.A. Engineering College Chennai.**
7	Participated in One day workshop on **"Hands on Training on LoRa (smart cities)"** held on 28th September 2019 at **Muthayammal Engineering College (Autonomous), Rasipuram.**
8	Participated in the **"One day Workshop on eSim, a First Course in the IOT Series and Teachers"** held at **Government College of Engineering, Salem on** 28st September 2019 organized by the **Teaching Learning Centre ICT at IIT Bombay**.
9	Participated in a Two-week Faculty Development Programme on **"Adaptive Array for Mobile Communication Applications"** sponsored by AICTE Conducted by **Muthayammal Engineering College, Rasipuram** from August 19-31, 2019.
10	Participated in the Induction Training Programme on **"Engineering Pedagogy for the 21st Century teachers"** organized by **Muthayammal Engineering College** on 23rd and 24th September 2016.
11	Participated in **Two- Week ISTE workshop on Control Systems** organized by **Indian**

	Institute of Technology Kharagpur from 2nd to 12th December 2014.
12	Participated in One day workshop on **"CMOS VLSI SYSTEM DESIGN"** held on 28th September 2013 at **Sengunthar Engineering College, Tiruchengode.**
13	Participated in One day workshop on **"RF Technologies, Measurements and Applications"** held on 16th March 2012 at **Sona College of Technology, Salem.**
14	Participated in One day workshop on **"EMERGING TRENDS IN DIGITAL IMAGE PROCESSING"** held on 6th August 2011 at **Sengunthar Engineering College, Tiruchengode.**
15	Participated in One day workshop on **"FPGA & ASIC System Design"** held on 24th September 2009 at **Sengunthar Engineering College, Tiruchengode.**