


A1	Personal Details	
Mr.S.Maragatharaj		
Assistant Professor		
Department of Electronics and Communication Engineering		
Knowledge Institute of Technology, Salem - 637 504. India.		
Ph: 9994791960		
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A2	Areas of Interest	Digital Electronics, VLSI Design
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A3	Subjects Taught		
	UG		PG
Digital Electronics	Electronic Circuits I	Low power VLSI Design	
VLSI Design	Electronic Circuits II		
Microprocessor and Microcontroller	Robotics		
Advanced Microprocessor	Computer Architecture and Organization		
Embedded and Real time Systems			

A4	Academic Background			
Degree	Specialization	Name of the Institute	University	Month and Year of Passing
M.E.	VLSI Design	Anna University of Technology, Coimbatore	Anna University, Coimbatore	June 2011
B.E.	Electronics & Communication Engineering	Annai Mathammal Sheela Engineering College	Anna University	Nov 2008

A5		Work Experience as on August 2021					
Name of the Institution		Position		From		To	
Knowledge Institute of Technology, Salem		Professor		July 2022		Present	
Dhirajlal Gandhi College of Technology		Assistant Professor		June 2019		May 2022	
Knowledge Institute of Technology, Salem		Assistant Professor		June 2012		May 2018	
The Kavery Engineering College		Assistant Professor		July 2011		May 2012	
Total Experience	10 Years 4 months	Teaching	10 Years 4 Months	Research	-	Administration	-

A6		Research Publications				
Total Number of Publications		International Journal	National Journal	International Conference	National Conference	Total
		10	-	6	1	17
Publications (Journals)						
S.No	Authors	Tile of the Article		Name of the Journal	Year, Vol., No.pages	
1.	S.Gopinath, S.Maragatharaj	Modified Routing Protocol for Defending Against Attacks in MANET		International Journal for Advanced Research in Computer Science and Software Engineering	Vol.2, Issue 1, 2012, pp.12-16.ISSN:2277128X	
2.	S.Maragatharaj	Paprr Reduction Based on Clipping and Scaling Approach in Multiplexing Systems Using Mat lab		International Journal of Communications and Engineering	Vol.5,Issue 2, March 2012.ISSN: 09880382E	
3.	Rathinakumar.K, Maragatharaj.S Saranya.N Saranya.S	A Novel Method Of Ambulance Service Time Reduction Using Zigbee		International Journal of Advance Research and Innovation	Volume 7 Issue 2 (2019) 102-105	
4.	S.Maragatharaj Kathiresan	Analysis of Different Bit Carry Look Ahead Adder Using verilog Code		International Journal of Electronics and Communication & Technology	Vol. 4, August 2013. ISSN: 2230 7109	

5.	S.Maragatharaj A. G. Sasikala	Simulation of QCA Based Binary to BCD Converter in Xilinx using HDLQ	imanager's Journal of Electronics Engineering	Vol. 1, August 2013.ISSN:2229 7286
6.	S.Maragatharaj A. G. Sasikala	QCA Based Low Power Parallel Binary Adder Subtractor using Reversible Logic Gates	imanager's Journal of Electronics Engineering	Vol. 1, February 2014. ISSN:2229 7286
7.	S.Maragatharaj R.Manoshri	Design of finite impulse response filter using distributed arithmetic of look up table	International journal of innovative research in science, engineering and technology	Vol.4 special issue 6, may 2015, ISSN: 2319 8753
8.	S.Maragatharaj, Soundhar Ganesh S, Venkatesh S, Vidhyasagar P	Raspberry Pi based interactive home automation system through Internet of things	International journal for research in applied science & engineering technology	Vol.3, Issue 11, March 2015, pp 809-814, ISSN: 2321 9653
9.	S.Maragatharaj Ragu B S T	FPGA Implementation of Area Efficient Finite Field Redundant Multipliers	International Journal for Scientific Research & Development,	Vol. 5, Issue 03, 2017
10.	S.Maragatharaj K.N.Vijeyakumar	"Efficient Energy Optimized Faithful Adder with Parallel Carry Generation	CMC- Computers,Materials&Continua	Vol.70, Issue 2, 2543–2561

International Conference

1.	S.Maragatharaj, Chrisjin Gnana Suji	Performance Analysis of Power Gating designs in Low Power VLSI Circuits	International conference on signal processing, communication, computing and networking technologies	February 2011
2.	S.Maragatharaj U.Hemima	Design of Energy and Area Efficient Low Power Carry Select Adder	International Conference on Advances in Engineering and Technology	May 2011
3.	S.Maragatharaj M.Dineshkumar	Enhanced Non-Traditional Learning Environment for Communication Engineers Using Free Open Source Software Tools	International Conference on Technology for Education	December 2013
4.	S.Maragatharaj A.G.Sasikala	Effective Binary to BCD Converter using Quantum Dot Cellular Automata	International Conference on Devices Circuits & Systems	January 2014
5.	S.Maragatharaj R.Manoshri	FPGA Implementation of FIR filter using Effective Distributed Arithmetic Algorithm	International Conference on Developments in Engineering Research	December 2014
6.	S.Maragatharaj, Soundhar Ganesh S, Venkatesh S, Vidhyasagar P	Raspberry Pi based Interactive Home Automation System through Internet of Things	International Conference on Recent Innovations in Engineering & Technology	March 2015

National Conference				
1	S.Maragatharaj S.Karthiga	Safety Helmet for Coal Miners	National Conference on Communication Technology Interventions for Rural and Social Development	March 2017

A8	Programs Organized
1.	Co-Organized six day AICTE-ISTE Sponsored Refresher/Induction Program on “Innovative Teaching and Outcome Based Education”, February 2022 at Dhirajlal Gandhi College of Technology, Salem.

A9	Consultancy Works
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A10	Highlights of Major Contributions
1.	Worked in various criteria's for NAAC and NBA Accreditation
2.	Coordinator for Major activates in Annual Day and Graduation Day and Placement Day (2018-2022)
3.	Class Advisor for the students and motivated in various ways.
4.	Robotics- Special Laboratory In charge

A11	Training Programmes Attended
1.	Participated in one day workshop on “Mentor Graphics based VLSI design flow” conducted by Anna University of Technology, Coimbatore
2.	Participated in six day Faculty Development Program on “VLSI Design” conducted by Jansons Institute of Technology, Coimbatore
3.	Participated in one day workshop on “Renesas Microcontrollers” conducted by Government College of Engineering, Salem
4.	Participated in one day workshop on “Ultra Low Power Mixed Signal Processor- MSP 430” conducted by Tenet Technologies, Bengaluru
5.	Participated in one day workshop on “Teacherpreneurship” conducted by Confederation of Indian Industries, Salem
6.	Participated in three day workshop on “Mission 10X” conducted by Wipro
7.	Participated in ten day workshop on “Analog Electronics” conducted by IIT- Kharagpur
8.	Participated in fifteen day workshop on “Advances in Medical Imaging Using Computational Intelligence”
9.	Participated in two day workshop on “Awareness and Training for Faculty about New Outcome Based Accreditation(OBA) process of NBA”, conducted by Jayam College of Engineering and Technology, Dharmapuri
10.	Co-Ordinated a two day national seminar on ”Real Time Embedded Solutions for Domestic Environment”
11.	Participated in ten day workshop on “Signals and systems” conducted by IIT- Mumbai
12.	Participated in ten day workshop on “Control Systems” conducted by IIT- Kharagpur

13.	Participated in two week workshop on “Environmental Engineering” conducted by IIT-Mumbai
14.	Participated in two week STTP on “CMOS, Mixed Signal and Radio Frequency VLSI Design”, conducted by IIT-Kharagpur
15.	Participated in AICTE sponsored two week FDP on “Network Security and CMOS Mixed Signal Architectures”, conducted by Dr. Mahalingam College of Engineering and Technology.
16.	Participated in workshop on “Journal Writing, IPR and Patent Drafting”, conducted by Karpagam College of Engineering, Coimbatore.
17.	Participated in AICTE Training and Learning (ATAL) Academy Online FDP on "Quantum Computing", conducted by Government College of Engineering, Trichy.
18.	Participated in 5 days online FDP on Computing & Signal Processing in IOT applications co-sponsored by IEEE SSIT society, Madras section, conducted by QIS College of Engineering And Technology, Andhra Pradesh.
19.	Participated in AICTE Training and Learning (ATAL) Academy Online FDP on “Robotics”, conducted by NIT Durgapur..
20.	Participated in AICTE Training and Learning (ATAL) Academy Online FDP on “Energy Engineering”, conducted by IIT Rourkee.
21.	Attended one day workshop on “CMOS IC Design, Process Integration and Fabrication”, conducted by Dr. Mahalingam College of Engineering and Technology.
22.	Participated in AICTE Training and Learning (ATAL) Academy Online FDP on “PROJECT THRIVE-Leadership Excellence”, conducted by IIM-Amritsar.
23.	Participated in AICTE Training and Learning (ATAL) Academy Online FDP on “Smart Production Technologies for Horticultural Crops (Combating climate change through Innovations and sustainability)” Conducted by National Institute of Food Technology Entrepreneurship and Management.
24.	Completed the 5-day online FDP on the theme “Inculcating Universal Human Values in Technical Education” organized by All India Council for Technical Education(AICTE).
25.	Completed MOOC course on “Education Technology for Engineering Teachers” conducted by IIT-Bombayx (13th March 2016)
26.	Participated & Selected for the final round of KALPANA CONTEST 2016, conducted by IIT-Mumbai and Tata Centre for Technology & Design. (25th May 2016)
27.	Completed MOOC course on “Academic Integrity” conducted by University of Auckland, New Zealand (03rd October 2016)
28.	Completed 12 week NPTEL course on “Patent Law for Engineers and Scientists” in January 2020.

A14	Patent Published
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